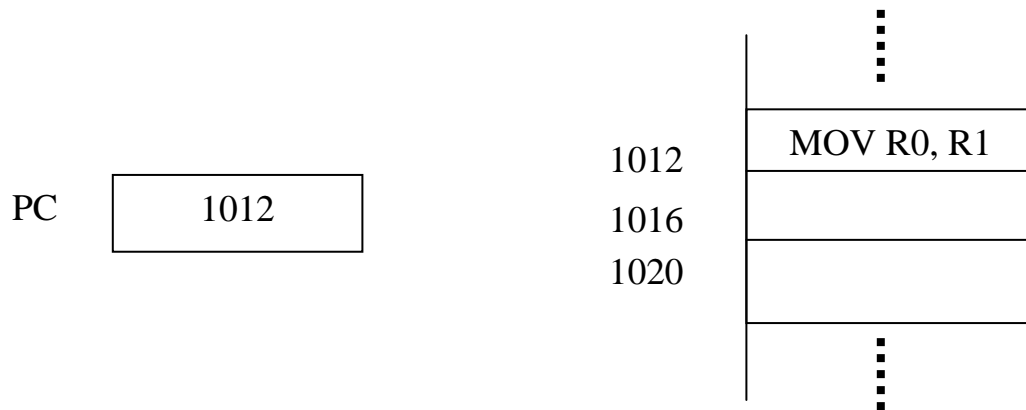


Sheet (2)

1. Assume the program counter (PC) is initially equal to n . Assume that the word length of the processor is 1.
 - (a) How many fetches are required to make PC equal to m if there are no branch instructions between n and m ?
 - (b) What is the content of the instruction register (IR) when the PC's value is $n+k$? Justify your answer.
2. Describe the fetch process for the configuration shown in the figure.



3. Problems 2.10 (a), 2.13 , 2.17, 3.29 (a) (b) from Hamacher.